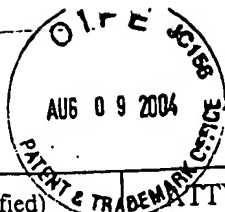


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**Form PTO-1449** (modified)

List of Patents and Publications  
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Disclosure Statement  
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ATTY. DKT. NO. **BP 2414 CON**

APPLICANT: **BRIAN T. CAMPBELL**

FILING DATE: **11/17/2003**

SERIAL NO. **10/715,311**

GROUP: **2818**

**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<i>JM</i>	A1	6,122,217	9/2000	Keeth et al.			
<i>JM</i>	A2	6,204,538	3/2001	Kim			
<i>JM</i>	A3	5,455,528	10/3/95	Partovi, et al.			
<i>JM</i>	A4	6,090,153	7/18/00	Chen, et al.			
<i>JM</i>	A5	5,896,046	4/20/99	Bjorksten, et al.			
<i>JM</i>	A6	5,568,076	10/22/96	Petella, et al.			
<i>JM</i>	A7	4,390,988	6/28/83	Best, et al.			
<i>JM</i>	A8	6,084,437	7/4/00	Sako			
<i>JM</i>	A9	5,455,802	10/3/95	McClure			

**FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

**OTHER ART** (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>JM</i>	A10	Weste, et al., "Principles of CMOS VLSI Design," A Systems Perspective, Addison-Wesley Publishing Company, June 1988, 5 pages.					

EXAMINER:

*SON MAI*

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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.